

**RESPONSE TO RESTRICTION REQUIREMENT**

Serial Number: 10/716,753

Filing Date November 19, 2003

Title: METHOD AND SYSTEM FOR COALESCING INPUT OUTPUT ACCESSES TO A VIRTUAL DEVICE

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Dkt: INT.P008

REMARKS

In the Office Action mailed September 15, 2005, claims 1-25 were indicated as being subject to a restriction requirement under 35 U.S.C. §121. Specifically, the Examiner states that

This application contains claims directed to the following patentably distinct species of the claimed invention:

Species 1, claims 1-9 and 18-22 are directed to performing virtualization by executing instructions from an instruction streams during a single virtualization event.

Species 2, claims 10-17 are directed to performing virtualization for determining whether a hash of block instructions matches a hash value in an address table.

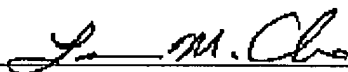
Species 3, claims 23-25 are directed to a virtualization event dispatcher including an instruction interpreter unit and an instruction scanning unit to designate additional instruction in an instruction block.

(9/15/05 Office Action, p. 2).

Applicants elect species 1, claims 1-9 and 18-22.

Respectfully submitted,

Dated: October 14, 2005



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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 14<sup>th</sup> day of October, 2005.

Chris Hammond

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Signature